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FACSIMILE TRANSMITTAL SHEET

In re application of: **Yeu-Der Chih**Examiner: **Huan Hoang**Serial No.: **10/785,522**Group Art Unit: **2827**Filed: **02/24/2004**Confirmation No.: **8308**

**For: AN ARRAY STRUCTURE OF TWO-TRANSISTOR CELLS WITH MERGED
FLOATING GATES FOR BYTE ERASE AND RE-WRITE IF DISTURBED
ALGORITHM**

TO: Examiner Huan Hoan
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Commissioner for Patents
P.O. Box 1450
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FACSIMILE NUMBER: 571-273-8300

CONFIRMATION
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MESSAGE: Attached is the Response To Office Action.

NOTE: Original will not follow

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Hgt
2/1/06